## CXTA27

# SURFACE MOUNT NPN SILICON DARLINGTON TRANSISTOR



www.centralsemi.com

# **DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CXTA27 type is a NPN Silicon Darlington Transistor manufactured by the epitaxial planar process, epoxy molded in a surface mount package, designed for applications requiring extremely high gain.

**MARKING: FULL PART NUMBER** 



MAXIMUM RATINGS: (T <sub>A</sub> =25°C)	SYMBOL		UNITS
Collector-Emitter Voltage	V <sub>CES</sub>	60	V
Emitter-Base Voltage	$V_{EBO}$	10	V
Continuous Collector Current	I <sub>C</sub>	500	mA
Power Dissipation	$P_{D}$	1.2	W
Operating and Storage Junction Temperature	$T_{J,} T_{stg}$	-65 to +150	°C
Thermal Resistance	$\Theta_{JA}$	104	°C/W

# **ELECTRICAL CHARACTERISTICS:** (T<sub>A</sub>=25°C unless otherwise noted)

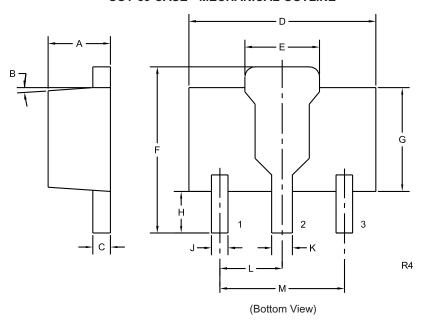
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I <sub>CBO</sub>	V <sub>CB</sub> =50V		100	nA
I <sub>CES</sub>	V <sub>CE</sub> =50V		500	nA
I <sub>EBO</sub>	V <sub>EB</sub> =10V		100	nA
BV <sub>CBO</sub>	I <sub>C</sub> =100μA	60		V
BV <sub>CES</sub>	I <sub>C</sub> =100μA	60		V
V <sub>CE</sub> (SAT)	I <sub>C</sub> =100mA, I <sub>B</sub> =0.1mA		1.5	V
V <sub>BE(ON)</sub>	$V_{CE}$ =5.0V, $I_{C}$ =100mA		2.0	V
h <sub>FE</sub>	$V_{CE}$ =5.0V, $I_{C}$ =10mA	10,000		
h <sub>FE</sub>	$V_{CE}$ =5.0V, $I_{C}$ =100mA	10,000		
$f_{T}$	$V_{CE}$ =50V, $I_{C}$ =10mA, f=100MHz	125		MHz

# CXTA27

# SURFACE MOUNT NPN SILICON DARLINGTON TRANSISTOR



# **SOT-89 CASE - MECHANICAL OUTLINE**



# LEAD CODE:

- 1) Emitter
- 2) Collector
- 3) Base

## MARKING:

**FULL PART NUMBER** 

DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
Α	0.055	0.067	1.40	1.70			
В	4°		4°				
С	0.014	0.018	0.35	0.46			
D	0.173	0.185	4.40	4.70			
E	0.064	0.074	1.62	1.87			
F	0.146	0.177	3.70	4.50			
G	0.090	0.106	2.29	2.70			
Н	0.028	0.051	0.70	1.30			
J	0.014	0.019	0.36	0.48			
K	0.017	0.023	0.44	0.58			
L	0.059		1.50				
М	0.118		3.00				

SOT-89 (REV: R4)

R4 (23-February 2010)

## **OUTSTANDING SUPPORT AND SUPERIOR SERVICES**



#### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

#### **DESIGNER SUPPORT/SERVICES**

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

#### REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

#### **CONTACT US**

## Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA

Main Tel: (631) 435-1110 Main Fax: (631) 435-1824

Support Team Fax: (631) 435-3388

www.centralsemi.com

Worldwide Field Representatives: www.centralsemi.com/wwreps

**Worldwide Distributors:** 

www.centralsemi.com/wwdistributors

For the latest version of Central Semiconductor's **LIMITATIONS AND DAMAGES DISCLAIMER**, which is part of Central's Standard Terms and Conditions of sale, visit: <a href="https://www.centralsemi.com/terms">www.centralsemi.com/terms</a>

www.centralsemi.com (001)